

## AMENDMENTS TO THE SPECIFICATION

Please amend the first paragraph of the specification on page 1 as follows:

The present application is related to and claims priority from and herein incorporates by reference the US provisional patent application serial number 60/445,872, entitled "Dual-Port Memory Using Single Port Memory for Multiple Clock Domain", by inventor, S. Babar Raza, filed on February 7, 2003. This present application is related to and herein incorporates by reference the US provisional patent application serial number 60/436,303, entitled "A Logic for Implementing a Dual Clock Domain Read Access with Predictable Timing for Bi-directional I/Os," by the same inventor, filed on December 31, 2002. The present application is related to and herein incorporates by reference the US non-provisional patent application serial number ~~xx/xxx,xxx~~ 10/746,349, entitled "Logic for Implementing a Dual Clock Domain Read Access with Predictable Timing for Bi-Directional Inputs/Outputs" by the same inventor, filed on December 24, 2003.